

Fig. 1A

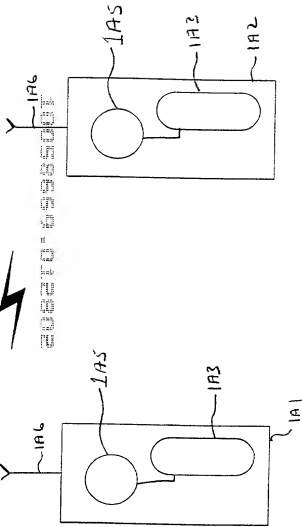
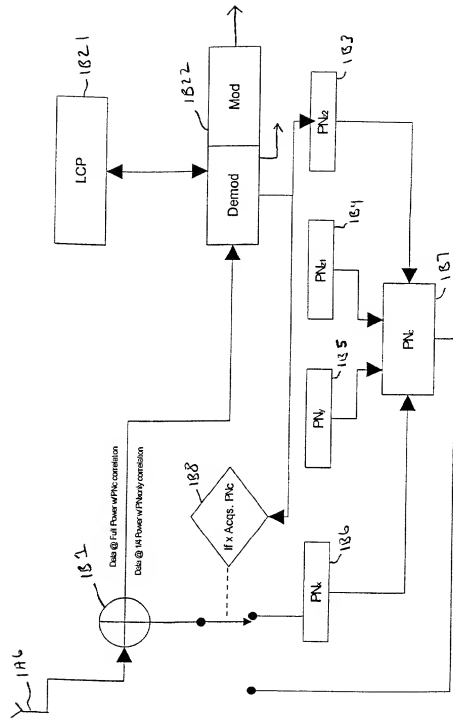


Fig. 1B



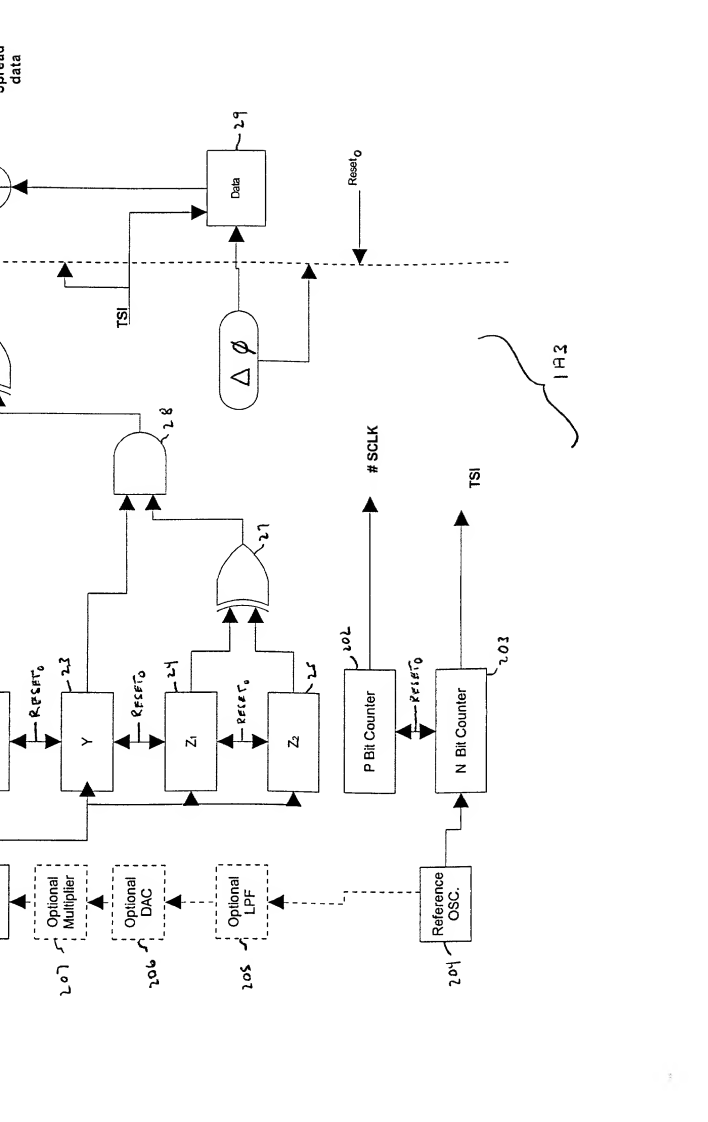
 Spread

Fig. 3A

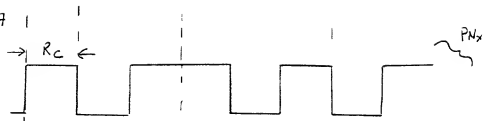


Fig. 3B

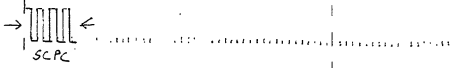


Fig. 3C

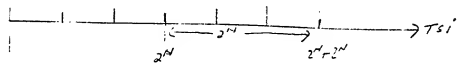


Fig. 4

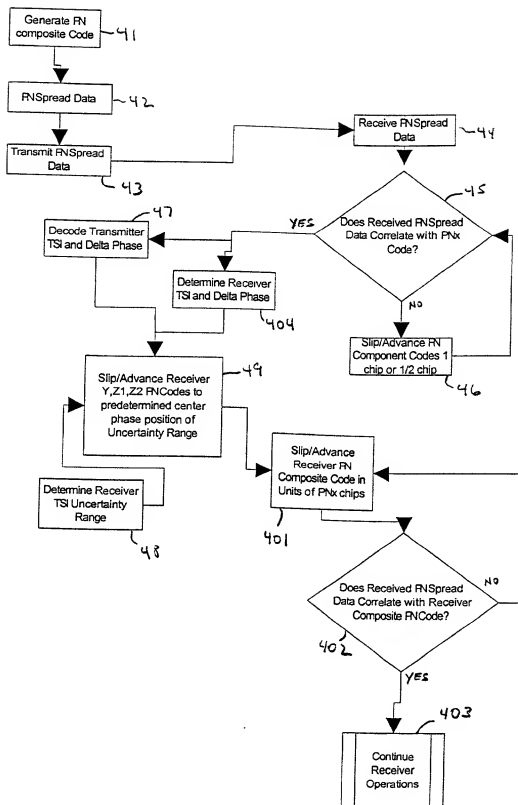


Fig. 5

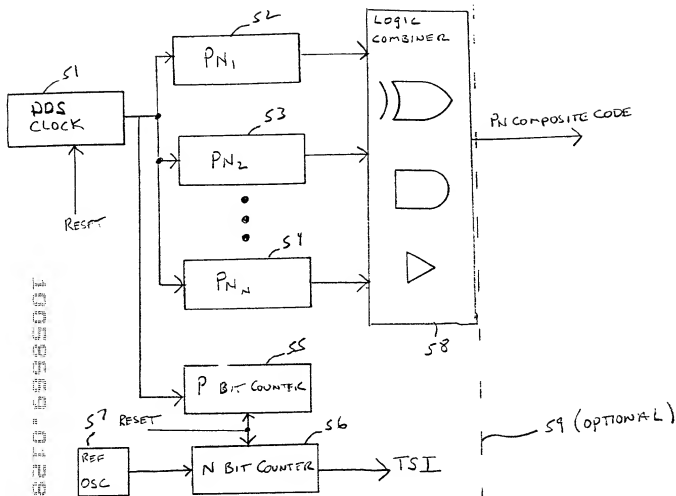


Fig. 6

TSI Latency/Uncertainty Budgets and Related PN Code Adjustments																
Process	WF A		WF B		WF C		WF D		WF E		WF F		WF G		WF H	
1) Status (min,max) msec	9	9	9	9	9	9	9	9	9	9	9	9	9	9	9	9
2) Bus Cycle Time (min, max) msec	0	130	0	130	0	130	0	130	0	130	0	130	0	130	0	130
3) Status Data Table (min,max) msec	0	57	0	57	0	57	0	57	0	57	0	57	0	57	0	57
4)Samp_rate, SPCPC a=893,353,198 b=1,108,674,225 c=1,165,482,325 d=1,342,320,446	d, 1		a, 1		c, 4		b, 2		b, 4		d, 4		d, 2		a, 2	
5) Local Status (msec), Bus Cycle Time Local (max, min = 0)	9	70	9	70	9	70	9	70	9	70	9	70	9	70	9	70
6) General Solution: Search Uncertainty, PN Code Advance msec	225	225	225	225	191	297	225	225	225	225	225	225	225	225	225	225
A) SIO (max) minimum = 0	1248b / 25.03 Kb/s		1248b / 25.03 Kb/s		1248b / 25.03 Kb/s		1248 / 20Kb/s		1248 / 20Kb/s		1248b / 25.03 Kb/s		1248b / 25.03 Kb/s		1248b / 25.03 Kb/s	
B) Interleave & Deinterleave Combined times m=0.2	2 <sup>14</sup> / 293 Kb/s		2 <sup>14</sup> / 390 Kb/s		512 / 15.9 Kb/s		2 <sup>14</sup> / 242 Kb/s		2 <sup>14</sup> / 242 Kb/s		2 <sup>14</sup> / 293 Kb/s		2 <sup>14</sup> / 293 Kb/s		2 <sup>14</sup> / 390 Kb/s	
C) Interleave & Deinterleave Combined times m=1	2 <sup>16-1</sup> / 293 Kb/s		2 <sup>16-1</sup> / 390 Kb/s		2 <sup>16-1</sup> / 15.9 Kb/s		2 <sup>16-1</sup> / 242 Kb/s		2 <sup>16-1</sup> / 242 Kb/s		2 <sup>16-1</sup> / 293 Kb/s		2 <sup>16-1</sup> / 293 Kb/s		2 <sup>16-1</sup> / 390 Kb/s	
D) Interleave & Deinterleave Combined times m=3	2 <sup>16-3</sup> / 293 Kb/s		2 <sup>16-3</sup> / 390 Kb/s		2 <sup>16-3</sup> / 15.9 Kb/s		2 <sup>16-3</sup> / 242 Kb/s		2 <sup>16-3</sup> / 242 Kb/s		2 <sup>16-3</sup> / 293 Kb/s		2 <sup>16-3</sup> / 293 Kb/s		2 <sup>16-3</sup> / 390 Kb/s	

Fig. 7

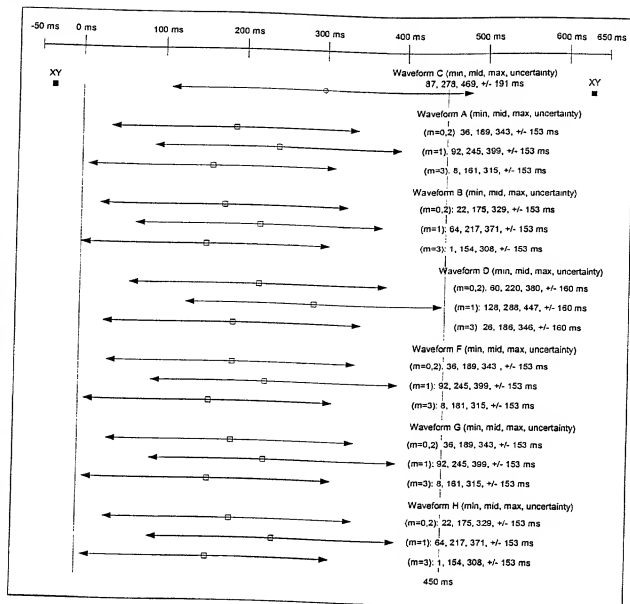


Fig. 8

